# Exact and Practical Modulo Scheduling for High-level Synthesis

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Loop pipelining is an essential technique in high-level synthesis (HLS) to increase the throughput and resource utilisation of FPGA-based accelerators. It relies on modulo schedulers to compute an operator schedule that allows subsequent loop iterations to overlap partially when executed, while still honouring all precedence and resource constraints. Modulo schedulers face a bi-criteria problem: minimise the initiation interval (II), i.e. the number of time steps after which new iterations are started, and minimise the schedule length.

We present Moovac, a novel exact formulation that models all aspects (including the II minimisation) of the modulo scheduling problem as a single integer linear program (ILP), and discuss simple measures to prevent excessive runtimes, to challenge the old preconception that exact modulo scheduling is impractical.

We substantiate this claim by conducting an experimental study covering 188 loops from two established HLS benchmark suites, four different time limits, and three bounds for the schedule length, to compare our approach against a highly-tuned exact formulation and a state-of-the-art heuristic algorithm. In the fastest configuration, an accumulated runtime of under 16 minutes is spent on scheduling all loops, and proven optimal IIs are found for 179 test instances.

CCS Concepts: • Hardware → Operations scheduling; *Reconfigurable logic and FPGAs*;

Additional Key Words and Phrases: modulo scheduling, exact, optimal, II minimisation, high-level synthesis

#### **ACM Reference format:**

Julian Oppermann, Melanie Reuter-Oppermann, Lukas Sommer, Andreas Koch, and Oliver Sinnen. 2019. Exact and Practical Modulo Scheduling for High-level Synthesis. *ACM Trans. Reconfig. Technol. Syst.* 12, 2, Article 8 (April 2019), 27 pages.

https://doi.org/10.1145/3317670

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1936-7406/2019/4-ART8 \$15.00

https://doi.org/10.1145/3317670

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## **1** INTRODUCTION

In contrast to software-programmable processors, which in the past have relied on increasing clock frequencies and in recent years switched to coarse-grain parallelism (multi-/many-core), field-programmable gate arrays (FPGA) focus on exploiting fine-grained instruction-level parallelism<sup>1</sup> to achieve high performance. A high-level synthesis (HLS) system that creates FPGA-based accelerators from sequential languages such as C must thus exploit all available sources of parallelism in order to achieve a meaningful speed-up compared to the execution on a software-programmable processor having a higher clock rate. One such source of parallelism is *loop pipelining*<sup>2</sup>: the partially overlapping execution of subsequent loop iterations intended to increase the accelerator's throughput and the utilisation of the functional units within the datapath corresponding to the loop's computation. To this end, new loop iterations are started after a fixed number of time steps, called the *initiation interval (II)*.

Let *T* be the latency of the datapath representing the loop body. Executing *n* iterations of the loop sequentially then takes  $n \cdot T$  time steps. Assuming that the loop's inter-iteration dependences allow it to be executed with an initiation interval  $\lambda < T$ , then executing *n* iterations will require only  $(n - 1) \cdot \lambda + T$  time steps, i.e. the last iteration is issued after  $(n - 1) \cdot \lambda$  time steps and ends after the *T* time steps to fully evaluate the result of the datapath. This means that the smaller the interval is relative to the latency of the datapath, the higher is the theoretical speed-up achievable through loop pipelining.

HLS-generated datapaths typically have to obey certain resource constraints, such as the number of requests a memory controller can handle in parallel. Additionally, operations relying on scarce FPGA resources, such as multipliers (called DSPs or DSP slices), might have to be time-multiplexed between different uses in the datapath. We call the operations requiring these constrained hardware blocks *resource-limited operations*.

After an initial warm-up time, a loop's datapath executes operations from different iterations in parallel. Therefore, it is no longer sufficient that an operation schedule fulfils the resource constraints just individually for each time step. With the overlap, the constraints now have to hold for entire congruence classes of time steps (step number modulo II).

Computing a (preferably small) feasible II and the associated operation schedule is called *modulo scheduling*. The corresponding mathematical problem that minimises the II is NP-hard [21].

This paper makes the following contributions to the field.

- We present Moovac<sup>3</sup>, an *exact* formulation in the sense that it models all aspects of the problem defined in Section 2.1, allowing it to compute an optimal solution. To the best of our knowledge, Moovac is the first formulation based on integer linear programs (ILP) to integrate the search for the optimal II, but is also competitive in a traditional, one-candidate-II-at-a-time setting. Moovac delivers high-quality results faster than both a state-of-the-art heuristic scheduler and a prior, highly-tuned ILP formulation.
- We investigate strategies to improve the practicability of exact modulo scheduling, namely employing different time limits and upper bounds for the schedule length. In this context, we propose improvements to an existing bound that lead to significantly better schedule length estimates. The effects of the different time limits and bounds on the schedulers' runtime and

<sup>&</sup>lt;sup>1</sup>Technically, this is *operator*-level parallelism, as computations are typically mapped to datapaths comprised of interconnected operator modules.

<sup>&</sup>lt;sup>2</sup>In the context of VLIW processors, the equivalent technique is usually called "software pipelining".

<sup>&</sup>lt;sup>3</sup>"Moovac" is an acronym for "**Mo**dulo **o**verlap **va**riable **c**onstraint".

Table 1. Problem signature for modulo scheduling

Input	
$O = \{1, \ldots, n\}$	operations
$D_i \in \mathbb{N}_0$	delay / latency of operation $i \in O$ (in time steps)
$E = \{(i \rightarrow j)\} \subseteq O \times O$ $d_{ij}, \ \beta_{ij} \in \mathbb{N}_0$ $\delta_{ij} = D_i + d_{ij}$	dependence edges edge delay, and dependence distance, of $(i \rightarrow j) \in E$ sum of edge's delay and its source operation's delay
$R = \{\text{mem, dsp,}\}$ $a_k \in \mathbb{N}$ $L_k \subseteq O$ $L = \bigcup_{k \in \mathbb{R}} L_k$	resource types available instances of resource type $k \in R$ resource-limited operations of type $k \in R$ union of all resource-limited operations
$\lambda^{\perp}, \ \lambda^{\top} \in \mathbb{N}$	lower bound and upper bound to the II search space
Output	
$\lambda^{\circ} \in \mathbb{N}$	a feasible initiation interval for the graph
$t_i \in \mathbb{N}_0, \ i \in O$	start time for operation <i>i</i>
$T_{\lambda^{\circ}} = \max_{i \in O} \{t_i + D_i\}$	schedule length for $\lambda^{\circ}$

solution quality are evaluated in an extensive experimental study covering 188 loops from two HLS benchmark suites.

A preliminary version of this work appeared as [24].

# 2 BACKGROUND

We will now formally define the modulo scheduling problem (MSP) and present prior scheduling approaches.

## 2.1 The modulo scheduling problem

The MSP is characterised by the signature in Table 1.

Input. The input comprises a directed graph consisting of a set of operations  $i \in O$  (nodes) with a fixed integer latency of  $D_i$ , and a set  $E \subseteq O \times O$  of directed edges  $(i \rightarrow j)$  modelling the dataflow and other precedence relations among the operations. The edges carry a delay  $d_{ij}$ . We define  $\delta_{ij} = D_i + d_{ij}$  to denote the sum of an edge's delay and its source operation's delay.

In addition to the usual intra-iteration dependences, a loop may contain inter-iteration dependences, also known as recurrences or loop-carried dependences. As the latter dependences point in the opposite direction of the normal dataflow, we also call them *backedges*. In order to handle both types of dependences uniformly, each edge is associated with a dependence *distance*  $\beta_{ij}$ that specifies how many iterations later the dependence has to hold. Intra-iteration dependences therefore have  $\beta_{ij} = 0$ , whereas inter-iteration dependences are characterised by  $\beta_{ij} \ge 1$ .

The operations *O* are carried out on resources (e.g. logic gates, DSPs, memories ...), of which some types, but not all, are limited in number<sup>4</sup>. Let *R* be the set of distinct resource types, whose

<sup>&</sup>lt;sup>4</sup>While in reality all resources are limited, some, e.g. logic gates, can be meaningfully considered unlimited on current FPGAs.



Fig. 1. Example problem



usage has to be limited when scheduling. We assume that every resource type  $k \in R$  provides  $a_k$  uniform and fully-pipelined *instances* that can accept new input data from at most one operation at any time. This means that at most  $a_k$  operations scheduled to start in time steps in the same congruence class (modulo II) can use resources of type k concurrently.

We further assume that every operation requires at most one limited resource of type k. We represent these resource-limited operations as members of the respective sets  $L_k$ . For brevity, we define L to be the set of all resource-limited operations.

Lastly, the search space for feasible and practically relevant IIs is bounded by the parameters  $\lambda^{\perp}$  (lower bound) and  $\lambda^{\top}$  (upper bound), which are explained in Section 2.2.

Figure 1 illustrates a complete MSP instance that will serve as a running example. It contains three operations (7), (9) and (11) (distinguished by their dark border) that compete for access to the single instance of resource type q, as well as two backedges  $((7) \rightarrow (11))$  and  $((12) \rightarrow (10))$  with a backedge distance of 1. If not stated otherwise, the operations have a delay of 1, and edges do not incur a propagation delay.

*Output.* We seek an initiation interval  $\lambda^{\circ}$  and an integer start time  $t_i$  for each operation, so that all dependence edges are honoured, i.e.

$$t_i + \delta_{ij} \le t_j + \beta_{ij} \cdot \lambda^\circ \quad \forall (i \to j) \in E, \tag{1}$$

and no resource type is oversubscribed in any congruence class (modulo II), i.e.

$$|\{i \in L_k : t_i \mod \lambda^\circ = m\}| \le a_k \quad \forall k \in R \text{ and } m \in [0, \lambda^\circ - 1].$$
<sup>(2)</sup>

For any feasible interval  $\lambda$ , the start times imply the schedule length  $T_{\lambda} = \max_{i \in O} \{t_i + D_i\}$ , i.e. the time step in which the last operation finishes. In particular, we are interested in the schedule length  $T_{\lambda^{\circ}}$  corresponding to the computed interval  $\lambda^{\circ}$ . The schedule length is conceptually equivalent to the latency of the datapath and the makespan of the problem graph.

Figure 2 shows a valid modulo schedule for the running example with an initiation interval of 3. The operations' start times  $t_i$  can be read off the column representing the first iteration. Observe that the backedge ( $(12)\rightarrow(10)$ ) is obeyed under the overlapping, and at any given time step, only one of the resource-limited operations is active.

*Objective.* Modulo scheduling is a bi-criteria optimisation problem. The first objective is to find the smallest II that satisfies (1) and (2). We refer to the optimal II according to this objective as  $\lambda^*$ . The second objective is to find a schedule for an interval  $\lambda$  with the minimal schedule length, denoted by  $T_{\lambda}^*$ .

In practice, minimising the II is far more important than minimising the schedule length, which implies an order of the objectives. To this end, the overall aim is to lexicographically minimise the tuple  $S = (\lambda^{\circ}, T_{\lambda^{\circ}})$ . The optimal solution to the MSP then is a schedule with  $S^{\star} = (\lambda^{\star}, T_{\lambda^{\star}}^{\star})$ .

## 2.2 Bounds for the II search space

Modulo schedulers typically consider one or more *candidate* IIs during their operation. The search space for sensible II values is defined by the following bounds.

A trivial **upper bound**  $\lambda^{\top}$  is the length of any resource-constrained *non-modulo* schedule. IIs larger than this value indicate that it would actually be faster to execute this loop in a non-overlapping manner. We use a non-modulo SDC scheduler [7] with heuristic resource constraints to quickly compute such a fallback schedule, and use its length to define  $\lambda^{\top}$ .

The **lower bound**  $\lambda^{\perp}$  is usually defined (e.g. in [25]) as  $\lambda^{\perp} = \max(\lambda_{\text{rec}}^{\perp}, \lambda_{\text{res}}^{\perp})$ , i.e. the maximum of the <u>rec</u>urrence-constrained minimum II and the <u>res</u>ource-constrained minimum II. Due to (1), the recurrences (cycles) in the dependence graph impose a lower bound for any feasible II. We compute  $\lambda_{\text{rec}}^{\perp}$  as the optimal solution to the following ILP<sup>5</sup>, defined for integer variables  $t_i$  that model the start time step for each operation *i*, and an integer variable  $\lambda$  that models the recurrence-induced II to be minimised.

$$\min \lambda \tag{3}$$

s.t. 
$$t_i + \delta_{ij} \leq t_j + \beta_{ij} \cdot \lambda \quad \forall (i \rightarrow j) \in E$$
 (4)

The resource-constrained minimum II is defined as  $\lambda_{\text{res}}^{\perp} = \max_{k \in R} \left[ \frac{|L_k|}{a_k} \right]$  and follows from (2). It is an application of the pigeonhole principle: A candidate interval  $\lambda$  cannot be feasible if the loop contains more operations using a limited resource *k* than can be assigned to the available resource instances in every modulo slot  $0 \dots (\lambda - 1)$ . Recall that we assume the operations itself to be fully-pipelined, meaning that they are able to accept new input values in every time step.

In the running example, we determine  $\lambda^{\perp} = 3$ : the recurrence spanned by backedge  $(12) \rightarrow (10)$  leads to  $\lambda_{\text{rec}}^{\perp} = 3$ . Also, we have  $\lambda_{\text{res}}^{\perp} = 3$  due to the presence of three resource-limited operations competing for access to one instance of type q. In this particular example, a simple as-soon-aspossible schedule has a length of 6 and can serve as a valid resource-constrained non-modulo schedule. Thus, we set  $\lambda^{\top} = 6$ .

# 2.3 Operator chaining

The scheduling problem at hand is defined in terms of time steps. The operations in a loop's datapath will be started according to their assigned start time step by a controller circuit that is also generated by the HLS tool.

Every combinatorial operation *i* (these operations have  $D_i = 0$ ) instantiated on the FPGA requires a non-zero amount of physical propagation time  $z_i$  (e.g. in nanoseconds) to complete. Typically, a

 $<sup>^{5}</sup>$  As noted by de Dinechin [8], this is a resource-free cyclic scheduling problem, which can be solved optimally in polynomial time.

HLS tool maintains a desired target cycle time Z for the datapath, which limits the time that can be spent in a single time step. Operations in different time steps are decoupled by registers that hold intermediate results.

However, not all operations require the same amount of time, e.g. logical operations and constant bit shifts are trivial to perform on an FPGA, and have  $z_i \ll Z$ . This allows HLS tools to schedule a *chain* of data-dependent operations into a single time step, as long as the *accumulated* physical delay of the operations in the chain does not exceed the desired cycle time.

We support operator chaining by allowing *chainable* operations  $i \in O$  with  $D_i = 0$ , and construct additional edges to prohibit excessive chaining right before the actual scheduling. The basic idea is to discover the longest (in terms of physical delay) combinatorial paths  $\chi_{u_1u_p} = (u_1, ..., u_p)$  with  $D_{u_l} = 0 \forall 1 \leq l \leq p$  between all pairs of a chainable operations  $u_1$  and  $u_p$ . Let  $\zeta_{u_1u_p} = \sum_{i \in \chi_{u_1u_p}} z_i$  be the accumulated physical delay along such a path. If  $Z < \zeta_{u_1u_p} \leq 2 \cdot Z$ ,  $u_1$  and  $u_p$  cannot be scheduled to the same time step, and therefore have to be separated by an edge  $(u_1 \rightarrow u_p)$  with  $d_{u_1u_p} = 1$ . In all other cases, no edge is needed, as either all operations of the chain can be scheduled to the same time step ( $\zeta_{u_1u_p} \leq Z$ ), or the separation of  $u_1$  and  $u_p$  is transitively guaranteed ( $\zeta_{u_1u_p} > 2 \cdot Z$ ). Additionally, all edges from chainable to non-chainable operations also carry an edge delay of 1.

Note that these edges correspond to the *cycle time constraints* proposed by Cong and Zhang [7]. However, the advantage of explicitly adding them a priori to the MSP, instead of inferring equivalent constraints when constructing the linear programs, is that the underlying path enumeration has to be performed only once and thus can be reused when computing the bounds of the II search space, and for each candidate II.

In the running example, a uniform physical propagation delay of 5 ns for the chainable operations (3) - (6), and a target cycle time of 10 ns, was assumed. Note the several edges with an edge delay of 1 that limit the amount of chaining to meet the desired cycle time.

#### 2.4 Related work

Loop pipelining is most useful when targeting processors with multiple parallel function units. Out-of-order processors rely on dynamic scheduling to derive the appropriate execution sequences at run-time. Parallel in-order processors (such as VLIW architectures), or statically scheduled hardware accelerators created by HLS, however, rely on the compiler/synthesis tool to pre-compute their execution schedules.

The underlying MSP is the same, with the primary objective to find a schedule with the minimally feasible II. However, VLIW modulo schedulers (e.g. [9, 12, 17, 21, 23, 25]) typically face tight resource constraints in each cycle (due to the limited number of functional units, registers, and buses). Often, minimising the usage of one or more of these resources, e.g. the register pressure, is used as the secondary objective instead of a schedule length minimisation.

Modulo schedulers targeting HLS (e.g. [5, 29]) face fewer resource constraints (hardware accelerators may use dozens or even hundreds of operators), but need to support operator chaining to achieve acceptable performance. Thus, when considering related work, not all techniques proposed for one target may be beneficial for the other. Especially heuristic approaches may incorporate the chosen secondary objective at the algorithmic level, making it harder to repurpose them for a different problem variant than approaches based on mathematical formulations.

In this work, we evaluate different approaches as measured by their suitability for *HLS modulo scheduling*.

Modulo schedules can be determined either by **heuristics** or by using mathematical formulations to solve the problem **exactly**. While heuristics are not guaranteed to find the optimal solution

(and often only produce feasible solutions), they usually come with a shorter computation time compared to the exact approaches, which are typically associated with impractically long runtimes.

*Heuristics.* Since Lam [21] and Rau [25] laid most of the groundwork of the modulo scheduling framework, many heuristic modulo schedulers have been proposed (e.g. [17, 23]). Codina et al. [6] explain the differences in the most relevant approaches, and provide a detailed performance evaluation. More recently, Zhang and Liu [29] implemented a modulo scheduler on top of a System of Difference Constraints (SDC) [7]. By construction, an SDC is represented by a totally unimodular constraint matrix, which guarantees that an optimal solution found by an LP solver will only consist of integer values for the decision variables. The flexibility of the SDC framework enables the easy integration of operator chaining into the modulo scheduling process. Improvements to this idea resulted in the Modulo SDC scheduler proposed by Canis et al. [5], which uses a modulo reservation table and a backtracking algorithm to handle the resource constraints heuristically, solving the underlying SDC multiple times in the process. We include this approach in our experimental evaluation as an advanced heuristic algorithm, and the representative of the state-of-the-art in HLS-specific modulo scheduling.

All known heuristic approaches attempt to minimise the II by trying to find a feasible schedule for various candidate IIs.

*Exact approaches.* Previously, the MSP has also been modelled exactly as an integer linear program (ILP) for a single candidate interval  $\lambda$ . Several formulations have been proposed that can be classified roughly according to their modelling of the operations' start times  $t_i$ , and handling of the resource constraints.

*Time-indexed* formulations use binary decision variables  $x_{i,t}$  per operation *i* and time step *t* to represent that *i* starts in *t*. The resource usage is constrained by comparing the sum of the  $x_{i,t}$  variables over the relevant time steps with the number of available resource instances.

The formulation by Dinechin [9] exclusively uses such binaries to model the time steps up to a predefined time horizon, i.e. an upper bound *U* for the schedule length, as  $t_i = \sum_{t=0}^{U} t \cdot x_{i,t}$ . As the formulation cannot be efficiently solved for large instances, a large neighbourhood search (LNS) heuristic is proposed. It speeds up the scheduling process as a schedule for  $\lambda - 1$  can easily be constructed (if it exists) given a feasible solution for  $\lambda$ .

In the formulation by Eichenberger and Davidson [12], the start times are decomposed according to the Euclidean division by  $\lambda$ , as  $t_i = y_i \cdot \lambda + \sum_{t=0}^{\lambda-1} t \cdot x_{i,t}$ . Here, binary decision variables model the assignment of the start time to the congruence classes  $0 \dots \lambda - 1$ , but integer variables  $y_i$  represent the so-called stage, i.e. the multiple of  $\lambda$  contained in  $t_i$ . The formulation models dependence edges as  $\lambda$ -many, but 0-1-structured constraints, which resulted in an almost tenfold speed-up over a prior version [13].

Ayala and Artigues [3] compare the aforementioned time-indexed formulations and report that the one by Eichenberger and Davidson [12] is always faster on their set of industrial instances. The authors obtain stronger variants of the formulations by applying a Dantzig-Wolfe decomposition, but rely on a column-generation scheme to cope with the increased number of decision variables.

Formulations using *integer* variables to directly model the (potentially decomposed) start times promise to work with fewer decision variables overall. Our proposed formulation, Moovac, is an extension to such a formulation from a task scheduling context by Venugopalan and Sinnen [28]. A common element in integer-based formulations is the concept of *overlap* variables, i.e.

binary variables that express an ordering relation on the values of the  $t_i$ . Similar formulations were proposed by Altman et al. [2] and Šůcha and Hanzálek [27].

In [2], the overlap scheme was inspired by a graph colouring problem. In contrast to Moovac, the set of constraints in the formulation is not independent of  $\lambda$ , which is a precondition for the integration of the II minimisation into the formulation.

The overlap mechanism in the formulation<sup>6</sup> by Šůcha and Hanzálek [27] differs semantically from ours: they define binary decision variables  $\hat{y}_{ij}$  to be 1 iff operations *i* and *j* are scheduled to the same congruence class and therefore cannot use the same resource instance. For every operation *i*, the number of conflicts *i* is involved in is counted and must be less than the number of available resource instances of a given type. In comparison, our formulation approach models conflicting resource instance and congruence class usage in a combined constraint that is defined pairwise. Besides being more concise, this allows for future extensions such as selectively allowing oversubscription of resource instances for mutually exclusive operations.

Fimmel and Müller [15] present an approach with II minimisation for a variant of the MSP where the II is allowed to be a rational number, which they claim to be beneficial especially for small IIs. Their formulation is a linear program comprised of binary and rational decision variables. The resulting schedules associate each operation with two iteration-dependent start time steps. While it would be possible to support such an execution model in an HLS tool, doing so would incur an additional multiplexing overhead. Also, it remains unclear how they modelled the rational variables in a standard ILP solver, which usually only support real- and integer-valued variables. Therefore, we do not include this approach in our evaluation.

In addition to ILPs, the MSP can be solved with an enumeration scheme and extensive pruning [1], or constraint programming (CP) [4, 11]. The CP framework allows to formulate more powerful constraints, and therefore needs less abstractions as an ILP model. For example, comparable to our proposed approach, Bonfietti et al. [4] also integrate the II minimisation into their scheduler. The CP-based approaches can achieve competitive or better performance compared to heuristics and ILP formulations by implementing highly problem-specific search strategies instead of the more generic branch-and-bound techniques typically employed in ILP solvers. On the other hand, implementing such a scheduler involves a significant engineering effort using a CP solver library, whereas an ILP model such as ours can be passed almost verbatim to the ILP solver's API. Also, ILP-based approaches may benefit from improvements to the solver's internal algorithms without modification.

From this variety of exact approaches, we compare our proposed formulation against the formulation by Eichenberger and Davidson [12], as it is a common reference point in the works cited above, and generally had a lasting impact on the field [14]. At the same time, it appears to be the bestknown alternative formulation to be solved by an unmodified ILP solver, due to its efficient internal structure. For brevity, we refer to this work as just "Eichenberger's" formulation in the rest of this paper.

# **3 THE MOOVAC FORMULATION**

We propose to tackle the MSP by including as much information as possible into an Integer Linear Program (ILP). To this end, we integrate the II minimisation into the formulation and obtain a bi-criteria problem that we call **Moovac-I**. As discussed in Section 2.4, there are various different

<sup>&</sup>lt;sup>6</sup>More precisely, our resource model with fully-pipelined operators corresponds to their unit processing time model.

ACM Transactions on Reconfigurable Technology and Systems, Vol. 12, No. 2, Article 8. Publication date: April 2019.

Т	$\in \mathbb{N}_0$		latest finish time (schedule length)						
$t_i$	$\in \mathbb{N}_0$	$\forall i \in O$	start time						
r <sub>i</sub>	$\in \mathbb{N}_0$	$\forall i \in L$	index of resource instance used by operation						
$m_i$	$\in \mathbb{N}_0$	$\forall i \in L$	index of congruence class (modulo II)						
$y_i$	$\in \mathbb{N}_0$	$\forall i \in L$	helper in congruence class computation						
£ <sub>ij</sub>	$\in \{0,1\}$	$\forall k: i, j \in L_k$	$\begin{cases} 1 & r_i < r_j \\ 0 & \text{otherwise} \end{cases}$						
$\mu_{ij}$	$\in \{0,1\}$	$\forall k: i, j \in L_k$	$\begin{cases} 1 & m_i < m_j \\ 0 & \text{otherwise} \end{cases}$						

Table 2. Moovac-S: Decision variables

approaches how to formulate a scheduling problem as an ILP, using different decision variables. Recent work on a related scheduling problem has shown that using overlap variables is more efficient than other approaches [28], so we take this approach here. Together with a linearisation of otherwise quadratic constraints, we expect to achieve a problem structure that can be solved in reasonable time for practically relevant problem instances. In addition, the *entire* runtime is used to determine a feasible and potentially optimal solution, while a practical downside of single-II approaches is that the time spent on *unsuccessful* candidate IIs is ultimately lost.

We also introduce a non-integrated variant of the Moovac formulation that follows the traditional approach to modulo scheduling and models a *single* scheduling attempt for a particular candidate II. Thus, we solve a formulation with only one objective, i.e. to minimise the schedule length. Solving the MSP then requires an external driver that traverses<sup>7</sup> the II search space until a feasible solution is found. This single-II variant of the Moovac formulation is called **Moovac-S**.

We start by defining the simpler formulation **Moovac-S** and then present the necessary extensions for obtaining the more general **Moovac-I** formulation.

## 3.1 Moovac-S

The problem signature of the Moovac-S formulation differs slightly from the general signature in Table 1, as the II minimisation is handled outside of the linear program that is introduced in the following sections. The bounds to the II search space,  $\lambda^{\perp}$  and  $\lambda^{\top}$ , are therefore passed to an external driver, which is also responsible to return the resulting II value  $\lambda^{\circ}$ . During the search, the driver chooses one or more candidate intervals  $\lambda$  that are passed to the ILP. Inside of the ILP,  $\lambda$  is a constant.

Decision variables. We model the problem with the decision variables shown in Table 2: As stated in the problem signature, the output we are seeking is a start time  $t_i$  for every operation  $i \in O$ . These variables are directly part of our ILP formulation. With the externally specified delay  $D_i$ , an operation i's result will be available in time step  $t_i + D_i$ . The variable T captures the latest finish time across all operations.

<sup>&</sup>lt;sup>7</sup>Traditionally, increasing candidate intervals  $\lambda = \lambda^{\perp}, \lambda^{\perp} + 1, \dots, \lambda^{\top}$  are tried. We discuss in Section 5.4 why this is still the most viable approach for our set of test instances.

ACM Transactions on Reconfigurable Technology and Systems, Vol. 12, No. 2, Article 8. Publication date: April 2019.

For every resource-limited operation  $i \in L_k$  using resource type k, we model the allocation of one of the  $a_k$  available instances to i with an integer variable  $r_i$ , which contains an index in the range  $[0, a_k - 1]$ .

Due to the nature of the modulo schedule, the start time of every operation  $t_i$  can be decomposed into a multiple  $y_i$  of  $\lambda$  plus a remainder  $m_i$  which is less than  $\lambda$ . The start time  $t_i$  is then  $t_i = y_i \cdot \lambda + m_i$ We define such variables for all resource-limited operations  $i \in L$ .  $m_i$  is the congruence class (modulo II) implied by the current start time  $t_i$  and is represented by an integer in the range  $[0, \lambda - 1]$ .  $y_i$ 's value is bound to the integer division  $t_i/\lambda$ .

Resource limit mechanism. A valid modulo schedule must not oversubscribe any of the resource types  $k \in R$  in any congruence class  $m \in [0, \lambda^{\circ}]$  (cf. (2) in Section 2.1). A common abstraction of this condition is the modulo reservation table (MRT) [25]. As illustrated in Figure 3, an MRT contains a row for each resource instance, and a column for each congruence class (modulo the current candidate II). Heuristic modulo schedulers often use an MRT as an explicit data structure, and successively allocate operations to the cells of the table. In order for a schedule to be valid, it must be ensured that each cell is occupied by at most one operation.

While we do not use an explicit MRT in the Moovac formulation, it is still a useful intuition, as the  $r_i$  and  $m_i$  variables belonging to an operation  $i \in L_k$  induce an MRT-like structure for resource type k. Our goal therefore is to model that no pair of operations  $i, j \in L_k$  shares an MRT cell. Formally, we wish to enforce

$$r_i \neq r_j \lor m_i \neq m_j \quad \forall i, j \in L_k, i \neq j.$$

However, the inequality relations as well as the disjunction need to be linearised for the ILP formulation.

To this end, we introduce the following *overlap* variables on all pairs of operations  $i, j \in L_k, i \neq j$  that use the same resource type  $k: \varepsilon_{ij} = 1$  indicates that *i*'s resource instance index is strictly less than *j*'s resource instance index. Analogously,  $\mu_{ij} = 1$  models that *i*'s congruence class index is strictly less than *j*'s congruence class index. With these variables, we can express  $r_i \neq r_j \Leftrightarrow r_i < r_j \lor r_i > r_j \Leftrightarrow \varepsilon_{ij} + \varepsilon_{ji} \ge 1$ , and  $m_i \neq m_j \Leftrightarrow \mu_{ij} + \mu_{ji} \ge 1$ . It follows that *i* and *j* are not in resource conflict if and only if  $\varepsilon_{ij} + \varepsilon_{ji} + \mu_{ij} + \mu_{ji} \ge 1$  is satisfied. Figure 4 demonstrates the interaction between two operations' *r* and *m* variables and their corresponding overlap variables.

Note that the externally specified resource limits  $a_k$  are modelled indirectly through the range of resource instance indices  $[0, a_k - 1]$  that can be assigned to the  $r_i$  variables, or in terms of the MRT intuition, through the number of rows that can be occupied by operations in each congruence class. In contrast to time-indexed formulations such as Eichenberger's, we do not explicitly count the number of limited operations in a particular congruence class to ensure (2).

*Constraints.* Using the decision variables defined in Table 2, the ILP formulation of Moovas-S is given in Figure 5. The default objective is to minimise the schedule length (M1).

We model the dependence edges with (M2), which are directly adopted from the precedence constraint (1) in the definition of the MSP.

As both sets of overlap variables are defined by a *strictly less* relation, for a given pair of operations  $i, j, \varepsilon_{ij}$  and  $\varepsilon_{ji}$ , as well as  $\mu_{ij}$  and  $\mu_{ji}$  cannot be 1 at same time. This is ensured by (M3) and (M6).

The overlap variables are bound to their desired values by the pairs of constraints (M4)+(M5) and (M7)+(M8), respectively. For brevity, we explain their function in the context of  $\mu_{ij}$ , as the constraints for  $\varepsilon_{ij}$  work analogously. (M7) are fulfilled if  $m_i < m_j$  or  $\mu_{ij} = 0$ , and (M8) are fulfilled if  $m_i \ge m_j$  or  $\mu_{ij} = 1$ . In these constraints, the second expression uses the candidate II as a big-M constant, meaning that its value is big enough to fulfil the constraint regardless of the rest of the





Fig. 3. Modulo reservation table as induced by the operations' *r*- and *m*-decision variables. Each resource instance can only be used by one operation per congruence class, thus each cell can be occupied by at most one operation.

Fig. 4. Consider two operations *i* and *j* that compete for the same MRT cell as indicated in Figure 3. This sketch shows the values of the overlap variables (white = '0', grey = '1') for different assignments of  $r_j$  and  $m_j$  in relation to  $r_i$  and  $m_i$ . For example, in the top right corner, we assume  $r_j = r_i - 1$  and  $m_j = m_i + 1$ , which is an assignment that does not result in a resource conflict.

expression. Due to the apparent contradictions, these constraints can only be fulfilled if and only if  $m_i < m_j$  and  $\mu_{ij} = 1$ , or  $m_i \ge m_j$  and  $\mu_{ij} = 0$ , resulting in the desired behaviour.

As derived above, constraints (M9) ensure a conflict-free resource usage for every pair of operations i, j, i.e. the operations are either assigned to different resource instances, mapped to different congruence classes, or both.

(M10) define the congruence class index  $m_i$  as expressed by the operation's start time  $t_i$  modulo II.

(M11) bound the resource instance indices for each limited operation of type k to be less than the externally specified limit  $a_k$ . Analogously, (M12) ensure that an operation's congruence class index is less or equal to the candidate II.

The latest finish time *T* is defined by (M13) to be greater or equal to the finish times of sink operations, i.e. operations that do not have outgoing forward edges  $(i \rightarrow j)$  with  $\beta_{ij} = 0$ .

(M14) - (M16) are domain constraints to enforce non-negativity respectively boolean values for the decision variables.

#### 3.2 Moovac-I

The Moovac-I formulation conforms to the problem signature in Table 1.

Note that the structure of the linear program as defined by the Moovac-S formulation is already independent of the concrete value of the candidate II: The set of decision variables and constraints is the same, and only the numerical values in the constraints differ between scheduling attempts. This is not the case in time-indexed formulations, such as Eichenberger's, where the numbers of binary variables  $x_{i,m}$  and constraints (e.g. (20) in [12]) vary with the candidate II.

(M1)

#### min T

 $r_i$ 

 $m_i$ 

s.t.  $t_i + \delta_{ij} \leq t_j + \beta_{ij} \cdot \lambda \quad \forall (i \rightarrow j) \in E$  (M2)

- $\begin{aligned} \varepsilon_{ij} + \varepsilon_{ji} &\leq 1 & \forall k \in R : \forall i, j \in L_k, i \neq j \\ r_j r_i 1 (\varepsilon_{ij} 1) \cdot a_k &\geq 0 & \forall k \in R : \forall i, j \in L_k, i \neq j \\ r_i r_i \varepsilon_{ij} \cdot a_k &\leq 0 & \forall k \in R : \forall i, j \in L_k, i \neq j \end{aligned}$ (M3)
- $\mu_{ij} + \mu_{ji} \leq 1 \qquad \forall k \in \mathbb{R} : \forall i, j \in L_k, i \neq j$  (M6)  $m_i m_i 1 (\mu_{ii} 1) \cdot \lambda \geq 0 \qquad \forall k \in \mathbb{R} : \forall i, j \in L_k, i \neq j$  (M7)

$$m_j - m_i - \mu_{ij} \cdot \lambda \qquad \leq 0 \qquad \forall k \in \mathbb{R} : \forall i, j \in L_k, i \neq j$$
(M1)

$$\varepsilon_{ij} + \varepsilon_{ji} + \mu_{ij} + \mu_{ji} \ge 1 \qquad \forall k \in \mathbb{R} : \forall i, j \in L_k, i \neq j$$
 (M9)

$$t_i \qquad \qquad = y_i \cdot \lambda + m_i \quad \forall i \in L \tag{M10}$$

$$\leq a_k - 1 \qquad \forall k \in R : \forall i \in L_k$$

$$\leq \lambda - 1 \qquad \forall i \in L$$
(M11)

$$t_i + D_i \leq T \qquad \forall i \in O : \nexists j \in O : (i \to j) \in E \land \beta_{ij} = 0 \quad (M13)$$

$$\begin{aligned} t_i & \in \mathbb{N}_0 & \forall i \in O \\ r_i, y_i, m_i & \in \mathbb{N}_0 & \forall i \in L \end{aligned}$$
(M14)

# $\varepsilon_{ij}, \mu_{ij} \in \{0, 1\} \quad \forall k \in \mathbb{R} : \forall i, j \in L_k, i \neq j$ (M16)

Fig. 5. Moovac-S: Objective function and constraints for a candidate interval  $\lambda$ 

In order to integrate the II minimisation into the Moovac-S formulation, we replace the formerly constant candidate II with a new integer decision variable  $\lambda$  that is bounded to the II search space as  $\lambda^{\perp} \leq \lambda \leq \lambda^{\top}$ . However, integrating the II minimisation naively has a major drawback: It results in quadratic (i.e. containing a multiplication of decision variables) constraints (M7), (M8) and (M10).

We linearise constraints (M7), (M8) by replacing the occurrence of  $\lambda$  with the upper bound of the II search space,  $\lambda^{\top}$ :

$$m_j - m_i - 1 - (\mu_{ij} - 1) \cdot \lambda^\top \ge 0 \quad \forall k \in \mathbb{R} : \forall i, j \in L_k, i \neq j$$
(M17)

$$m_j - m_i - \mu_{ij} \cdot \lambda^{\mathsf{T}} \leq 0 \quad \forall k \in \mathbb{R} : \forall i, j \in L_k, i \neq j$$
(M18)

This has no effect on the constraints' functionality, as the interval value is used as a big-M constant here.

The remaining quadratic constraints (M10) are broken down into individual constraints for all possible values of  $y_i$ . To this end, we need to introduce an upper bound Y for the  $y_i$  variables. Recall that the  $y_i$  variables represent the value  $\lfloor \frac{t_i}{\lambda} \rfloor$  in the calculation of operation *i*'s congruence class.

v

Given an upper bound *U* for the schedule length<sup>8</sup> (and in consequence, for all  $t_i$ ), and using the II search space's lower bound  $\lambda^{\perp}$ , the  $y_i$  variables are bounded by  $Y = \left| \frac{U}{1^{\perp}} \right|$ .

With this bound and introducing new binary variables  $\gamma_{\bar{y}i}$  for  $\bar{y} \in [0, Y]$  and all resource-limited operations, constraints (M10) are replaced by:

$$\gamma_{\bar{y}i} = 1 \quad \to t_i = \bar{y} \cdot \lambda + m_i \quad \forall \bar{y} \in [0, Y] : \forall i \in L$$
(M19)

$$\sum_{\bar{y}=0}^{I} \gamma_{\bar{y}i} \ge 1 \qquad \qquad \forall i \in L \tag{M20}$$

$$\sum_{\bar{y}=0}^{Y} \bar{y} \cdot \gamma_{\bar{y}i} = y_i \qquad \qquad \forall i \in L \tag{M21}$$

The indicator<sup>9</sup> constraints (M19) conditionally model the modulo decomposition for every possible value of  $y_i$ . Constraints (M20) force that at least one of these linearised decompositions is selected for the solution. Note that the  $\geq$ -constraints are sufficient here, as at most one  $\gamma_{y_i}$  can be non-zero due to the mutually-exclusive nature of the decomposition. Lastly, constraints (M21) define the value of  $y_i$  according to the selected decomposition. These constraints are not required for the correctness of the Moovac-I formulation, but make solutions interchangeable between Moovac-I and Moovac-S, a property we leverage in Section 4.1.

The MSP's bi-criteria objective can now be modelled directly in the Moovac-I formulation, which is defined by:

$$\min \lambda \tag{M22}$$

## 4 STRATEGIES FOR MODULO SCHEDULING IN PRACTICE

We now discuss the use of time limits, and bounds on the schedule length. A time limit allows to cap the worst-case solution times, whereas a schedule length bound is required to make the integration of the II search in the Moovac-I formulation possible, and can help to narrow down the solution space.

#### 4.1 Time-limited operation

Since modulo scheduling is an NP-hard problem [21], we cannot rule out the possibility to encounter problem instances that lead to exponential runtimes when solved with the exact approaches, or cause the heuristic approach to get stuck.

To this end, we impose a time limit  $\tau$  per candidate II. For all approaches, this includes the time  $\kappa$  to construct the linear program via the solver's API. Combining the exact approaches with a time limit can lead to non-optimal solutions, however, we retain the ability to make statements about the optimality of the returned solutions, as discussed in the following paragraphs.

*Moovac-S, Eichenberger.* Each scheduling attempt with the single-II Moovac formulation as well as with Eichenberger's formulation is executed as one invocation of the ILP solver, so a time limit of  $\tau - \kappa$  can directly be specified via its API.

<sup>&</sup>lt;sup>8</sup>We discuss possible upper bounds in Section 4.2.

<sup>&</sup>lt;sup>9</sup>An indicator constraint is of the form  $x = f \rightarrow \langle cons \rangle$ : if the binary variable x has the value f, the right-hand side constraint  $\langle cons \rangle$  must be satisfied; otherwise, it may be violated. Indicator constraints are supported natively in modern ILP solvers.

When the solver returns from a scheduling attempt for a candidate interval  $\lambda$ , it reports one of the following outcomes:

- Optimal. A schedule was found, and the solver *proved* that its length  $T_{\lambda}$  is optimal, i.e.  $T_{\lambda} = T_{\lambda}^{\star}$ .
- Feasible. A schedule with length  $T_{\lambda}$  was found, but the solver was unable to determine whether it is optimal within the time limit. We only know that  $T_{\lambda} \ge T_{\lambda}^{\star}$ .
- Unknown. No schedule was found within the time limit. We conservatively consider  $\lambda$  to be infeasible.
- Infeasible. The solver proved that the attempt is infeasible.

Let  $\lambda^{\circ}$  be the smallest II for which at least a feasible modulo schedule was found.  $\lambda^{\circ}$  is the optimal II if, trivially,  $\lambda^{\circ} = \lambda^{\perp}$ , or if all candidates  $\lambda^{-} \in [\lambda^{\perp}, \lambda^{\circ})$  are known to be infeasible. In case scheduling for  $\lambda^{\circ}$  yielded an optimal schedule length, we have also found an overall optimal solution  $S^{\star} = (\lambda^{\circ}, T_{\lambda^{\circ}}^{*})$ , otherwise we only have  $\lambda^{\circ} = \lambda^{\star}$ .

*Modulo SDC.* The Modulo SDC algorithm starts a scheduling attempt with computing a nonresource constrained schedule, and iteratively tries to assign resource-limited operations to their designated time steps. The modulo reservation table (MRT) is queried for resource conflicts, and if no conflicts arise, an operation's assignment is fixed in the underlying SDC by adding new equality constraints. Otherwise, a new constraint to move the conflicting operation to the next time step is introduced. The SDC is solved afterwards to check the feasibility of the current partial schedule. Should the schedule become infeasible, the algorithm uses backtracking to revert some of the previous assignments and resumes.

It is apparent that such a scheduling attempt necessitates constructing and solving multiple linear programs. We thus do not impose our time limit on the individual invocations of the LP solver, but instead on the scheduling attempt as a whole, i.e. including the solver runtimes and all MRT and backtracking operations.

In case the current candidate II is infeasible, the algorithm would potentially run until all possible schedules were inspected. To that end, the algorithm maintains a budget of  $6 \cdot |O|$  backtracking steps [5] to provide another failsafe for fruitless scheduling attempts. This mechanism is still in place in our implementation, as otherwise, even simple problem instances would deplete the whole time budget on infeasible candidate IIs.

The only situation in which an interval  $\lambda^{\circ}$  returned by the Modulo SDC scheduler is known to be optimal is if  $\lambda^{\circ} = \lambda^{\perp}$ , as the algorithm can only run out of time or backtracking steps, but never prove infeasibility of a candidate II.

The algorithm contains no means to determine the optimality of the found schedule length.

*Moovac-I.* The integration of the II search into the Moovac-I formulation enables a different solution strategy. Instead of performing the minimisation of the first objective (= the initiation interval) by iteratively solving and minimising multiple ILPs for the second objective (= here, the schedule length), we can optimise both objectives directly using a single Moovac-I-ILP.

ILP solvers handle multiple objectives either by attempting to solve the problem only once with user-specified weights for the objective functions, or by addressing the different objectives in individual steps, according to priorities given by the user. The latter, multi-step approach is more suitable in the context of the modulo scheduling problems, because practitioners would almost certainly choose weights that strongly favor better IIs anyway, as discussed in Section 2.1. Some ILP solvers offer an API to perform multi-objective optimisation automatically. However, in order for our approach to be solver-independent, and because it makes it easier to reason about the solution quality in the presence of time limits, we implement the multi-step approach ourselves.

To this end, we begin by setting a time limit of  $\tau$  and instruct the solver to only minimise the II. When the solver returns, it reports one of the following outcomes:

- Optimal. A schedule for an interval  $\lambda^{\circ}$  was found and *proven* to be optimal, i.e.  $\lambda^{\circ} = \lambda^{\star}$ .
- Feasible. A schedule for an interval λ° was found, but the solver was unable to determine whether it is optimal within the time limit. We only know that λ° is optimal if it is equal to λ<sup>⊥</sup>.
- Unknown. No schedule was found within the time limit; give up and report failure.

The interval computed in the first step,  $\lambda^{\circ}$ , is fixed during the second step. This allows us to reduce the complexity of the current ILP by "downgrading" it to resemble the Moovac-S formulation. We add a constraint to bind the decision variable  $\lambda$  to  $\lambda^{\circ}$ , and replace the constraints (M19)-(M21) by the simpler Moovac-S-style version of the modulo decomposition,  $t_i = y_i \cdot \lambda^{\circ} + m_i$ ,  $\forall i \in L$ .

Again with a time limit of  $\tau$ , the solver is now instructed to minimise only the schedule length. Note that this is a "warm start" made possible by constraints (M21), meaning the feasible solution from the first step is still valid for the modified ILP, and the solver will work on improving it. Therefore, only two outcomes are possible after the solver returns the second time:

- Optimal. The solver *proved* that the current schedule's length  $T_{\lambda^{\circ}}$  is optimal, i.e.  $T_{\lambda^{\circ}} = T_{\lambda^{\circ}}^{\star}$ .
- Feasible. The current schedule has a length of  $T_{\lambda^{\circ}}$ , but the solver was unable to determine whether it is optimal within the time limit. We only know that  $T_{\lambda^{\circ}} \ge T_{\lambda^{\circ}}^{\star}$ .

The reasoning about the optimality of the overall solution  $S = (\lambda^{\circ}, T_{\lambda^{\circ}})$  is straight-forward: We have  $S = S^{\star}$  if and only if both steps yielded an optimal result according to their respective objective. If optimality can be proven in the first step, but not in the second step, we trivially know  $\lambda^{\circ} = \lambda^{\star}$ . It is possible that we just find a feasible interval, but determine the optimal schedule length for that interval. This situation is counted as an overall feasible solution.

*Fallback schedule.* Recall that we save a resource-constrained non-modulo schedule from the computation of  $\lambda^{\top}$ . This schedule serves as a fallback in the unfortunate case that all modulo scheduling attempts fail, and allows the compilation to continue after a guaranteed maximum time.

## 4.2 Bounded schedule length

Imposing an upper bound U on the schedule length is beneficial for practical modulo scheduling, as it reduces the overall solution space of the combinatorial problem, and helps the LP solver to give up earlier on fruitless branches in situations where the infeasibility of a partial solution is not obvious to the solver, causing it to try increasingly late start times for operations in the search. Additionally, such a bound is required to make the proposed linearisation of constraints (M19) in the Moovac-I formulation possible.

While we desire *U* to be as tight as possible, any chosen bound needs to be an overestimate, i.e. higher or equal to the optimal schedule length for a given  $\lambda$ , to enable the solver to find a feasible solution for the given  $\lambda$  (and not to rule that  $\lambda$  out incorrectly).

*Eichenberger's bound.* Eichenberger et al. [13] proposed and proved the following upper bound for the length of a modulo schedule, which we adapt here to our notation. Let  $\Delta = \max_{(i \to j) \in E} \delta_{ij}$ , i.e. the maximum number of time steps that two operations connected by a dependence edge must be started apart. We define

$$U_{Eb} = |O| \cdot (\Delta + \lambda^{\top} - 1) .$$
<sup>(5)</sup>



Fig. 6. Distribution of modulo schedule lengths, and their bounds. Note the logarithmic scale on the Y-axis.

Intuitively, each operation  $i \in O$  requires at most  $\Delta$  time steps before the next dependent operation can start, but may need to be deferred for up to  $\lambda^{\top} - 1$  time steps<sup>10</sup> due to the modulo resource constraints.

Improved bound. Figure 6 shows the distribution of best-known schedule lengths, and corresponding bound values for  $U_{Eb}$ , for the test instances used in our experimental evaluation (Section 5.4). It is obvious that  $U_{Eb}$  overestimates the actual schedule length by roughly an order of magnitude. We propose two improvements to Eichenberger's bound that result in a safe upper bound  $U_{Im}$  that is much closer to the actual modulo schedule length (also shown in Figure 6).

Let  $\Delta_i = \max_{j \in O: (i \to j) \in E} \delta_{ij}$  denote the maximum number of time steps that any successor *j* of *i* needs to be started after *i*. We define the improved bound as

$$U_{Im} = \sum_{i \in O} \Delta_i + \sum_{k \in R} \sum_{q=0}^{|L_k|-1} \left\lfloor \frac{q}{a_k} \right\rfloor$$
 (6)

a) assume sequential schedule b) account for shifts due to modulo resource conflicts

This definition keeps the basic ideas in Eichenberger's bound of a) assuming that in the worst case, all operations need to be scheduled sequentially, and b) of accounting for the need to shift operations to a later start time due to the modulo resource conflicts.

Our improvement to a) is straight-forward: Instead of assuming |O|-many same-sized windows of time steps that are large enough to accomodate every operation in the MSP, we determine the maximum number of time steps individually for each operation.

We now quantify the worst-case number of modulo resource conflicts more precisely to derive a tighter estimate for b). First, unlimited operations never need to be shifted as, by definition, they never compete for resources. Next, recall the intuition of the modulo reservation table (Figure 3) for a resource type k with one available instance, and imagine it is successively filled with the

<sup>&</sup>lt;sup>10</sup>More accurately, given a candidate interval  $\lambda$ , an operation  $i \in L$  may be deferred for  $\lambda - 1$  time steps, because at that point all possible congruence classes would have been considered. Choosing the upper bound instead results in a slightly looser bound, but makes the individual scheduling attempts better comparable as the numerical value of the bound remains constant.

operations  $\{l_0, \ldots, l_{|L_k|-1}\} = L_k$  competing for that k-instance during scheduling<sup>11</sup>. Operation  $l_0$  will not need to be shifted as it is the first operation to be assigned to the MRT.  $l_1$  may need to be shifted for at most one time step if it conflicts with  $l_0$ .  $l_2$  may need to be shifted for at most two time steps if it conflicts with  $l_0$ , and then with  $l_1$ , which was already shifted by one time step to resolve its own conflict with  $l_0$ . In the worst case, all operations in  $L_k$  are in initially in conflict for using the k-instance in the same congruence class. In general, an operation  $l_q$  will need to be shifted for at most q time steps, i.e. equal to the number of operations that are already assigned to the MRT.

This reasoning is easily extended to resource types k that provide  $a_k > 1$  available instances. In that case, the required conflict-resolving shift amount increases only every  $a_k$  operations, which means that an operation  $l_q$  will need to be shifted at most  $\left\lfloor \frac{q}{a_k} \right\rfloor$  time steps in the worst case.

Summing the maximum shift amounts for all operations over all limited resource types, we arrive at the expression in Eq. (6-b).

Note that we neither make any assumptions about the particular conflict-resolving shift amount an individual operation might require, nor about the order in which operations are assigned to the MRT, but rather estimate the maximum number of shifts to be expected for a set  $L_k$  as a whole.

For the running example (Figure 1), we compute  $U_{Eb} = 12 \cdot (2 + 6 - 1) = 84$ , and  $U_{Im} = (11 \cdot 1 + 1 \cdot 2) + (0 + 1 + 2) = 16$ .

## 5 EXPERIMENTAL EVALUATION

We now compare scheduler implementations based on the Moovac-S, Moovac-I and Eichenberger's formulations, and the Modulo SDC scheduler, on a large set of typical high-level synthesis loops, in terms of both the scheduling time and the quality of the resulting schedule lengths and minimal IIs.

#### 5.1 Compiler context

We implemented all schedulers in the Nymble HLS compiler [18]. Nymble is based on the LLVM framework [22], version 3.3 and uses the framework's analyses and optimisations.

All function calls in the benchmark programs are inlined exhaustively. The resulting modules are optimised with LLVM's preset -O2, but without performing loop unrolling.

Intermediate representation. Nymble uses a hierarchical control-dataflow graph (CDFG) as its main intermediate representation, i.e. the compiler constructs a CDFG for each natural loop in the input program. Within such a graph, control information is translated to predicated dataflow, and nested loops are represented as special operations. This IR allows us to attempt to modulo schedule loops on all nesting levels and with arbitrary control structures, without the need to perform preparative transformations such as if-conversion.

We rely on LLVM's dependence analysis to discover intra- and inter-iteration memory dependences. These dependences are encoded as additional edges in the CDFG, and handled uniformly by the schedulers. Due to a technical limitation, we currently consider all of these backedges to express loop-carried dependences to the *immediately preceding* iteration, leading to conservatively larger IIs. However, we expect the impact on our results to be small, as in our compiler context, the dependence analysis could only determine exact backedge distances  $\beta_{ij} > 1$  for less than 0.3 % of the backedges that were constructed.

The schedulers operated according to the resource limits in Table 3.

<sup>&</sup>lt;sup>11</sup>Note that while the ILP-based schedulers discussed in this work do not operate in such a way internally, it is still a helpful conceptual model.

Table 3. Resource limits

```
Table 4. Problem sizes
```

Resource type	# available	Metric	min.	med.	avg.	max.
Memory Load/Store	1 each	# operations	19	54	125	2654
Nested loops	1	# reslim. ops.	0	4	12	221
Integer Div/other	$8/\infty$	<pre># forward edges</pre>	28	81	245	6752
FP Add/Sub/Mul/other	4/4/4/2 each	# backedges	2	4	42	1222

## 5.2 Reference schedulers

We implemented the Modulo SDC scheduler according to [5], but used a simpler height-based priority function. Our implementation of Eichenberger's formulation is based on the constraints (1), (2), (5) and (20) in [12]. In both cases, the objective is changed to minimise the schedule length.

The dataflow graphs constructed by Nymble contain a unique start operation that is required to be scheduled to time step 0. A corresponding constraint is added to all schedulers.

## 5.3 Test setup

We used Gurobi 8.0 as (I)LP solver for all schedulers.

The experiments were conducted on 2x12-core Intel Xeon E5-2680 v3 systems running at 2.8 GHz with 64 GiB RAM. The schedulers were allowed to use up to 8 threads and 16 GiB of memory per loop.

Each experiment was repeated three times to compensate for varying system load, and for each loop, we include in our evaluation the result of the "best" modulo scheduling attempt with regard to the smallest II, schedule length, and lastly, scheduling runtime. The scheduling runtime always includes the time to construct the respective linear programs via the Gurobi API. The generated schedules were verified by RTL simulation of the hardware accelerators generated by Nymble.

## 5.4 Test instances

The modulo scheduling test instances, i.e. loops/graphs, used in this evaluation originate from the HLS benchmark applications in the CHStone [16] and MachSuite [26] collections. We excluded backprop, bfs/bulk, fft/transpose and nw from MachSuite due to limitations in Nymble that prohibited the synthesis of these applications even without using the modulo schedulers evaluated here. Also, we removed printf statements inside the computational kernels of CHStone's aes and jpeg applications.

In total, we obtained 354 loops to modulo schedule. Due to the exhaustive inlining performed in Nymble, and the presence of small, idiomatic loops (e.g. array initialisations), we detected that 166 loops are identical or isomorphic to other loops.

The remaining **188 loops** comprise our set of test instances used in the rest of this evaluation. Table 4 characterises the sizes of the corresponding MSPs. The histogram in Figure 7 shows the distribution of the best-known intervals  $\lambda^{\bullet}$  for the test instances, normalised to the range between  $0 \ (= \lambda^{\perp})$  and  $1 \ (= \lambda^{\top})$ . In summary, the majority of loops in our test set have a best-known II equal or close to the lower bound of the search space. To this end, the single-II approaches in this evaluation traverse the II search space in the traditional ascending order. In order to be able to complete each individual experiment within 24 hours, we limit the search to at most 23 candidate IIs for the single-II approaches, i.e. we attempt to schedule for  $\lambda^{\perp}$ ,  $\lambda^{\perp} + 1$ ,  $\lambda^{\perp} + 2$ , ...,  $\lambda^{\perp} + 22$ . All loops in our



Fig. 7. Histogram of the number of loops according to the relative position of their  $\lambda^{\bullet}$  within the II search space, i.e.  $0 = \lambda^{\perp}$  and  $1 = \lambda^{\top}$ .

evaluation have  $\min(\lambda^{\top}, \lambda^{\bullet}) \leq \lambda^{\perp} + 22$ , i.e. this attempt limit did not preclude any of the schedulers from finding a feasible solution.

## 5.5 Comparison of approaches, time limits and bounds

In our first experiment, we schedule the test instances with the single-II Moovac formulation (denoted by *Mv-S*), the Moovac formulation with integrated II minimisation (*Mv-I*), Eichenberger's formulation (*EB*), and the Modulo SDC algorithm (*MSDC*).

We investigate time limits (Section 4.1)  $\tau$  of 1, 5, 15 and 60 minutes, and impose either no upper bound on the schedule length (denoted by  $\infty$ ), or use the bounds  $U_{Eb}$  or  $U_{Im}$  from Section 4.2.

While the time limit and schedule length bound seem independent on first sight, they both limit the computational effort that the solver spends on a particular scheduling attempt. In the case of the schedule length bound, this limit works indirectly by reducing the branch-and-bound solution space.

To this end, we present the results of modulo scheduling with the different time limits and upper bounds together, both in terms of scheduler runtimes (Table 5) and schedule quality (Table 6). Our methodology here is twofold: On the one hand, we provide accumulated runtimes and quality measures for the computed initiation intervals across all instances as intuitive metrics for the performance of a given configuration. However, this presentation disregards that all instances are in fact independent of each other. To this end, we additionally count the number of instances for which a configuration achieves a particular result, e.g. to schedule an instance between 10 and 100 seconds, or to prove optimality for a solution. This allows us to assess to which degree a given configuration is practical on our representative benchmark set, and highlights the presence of outlier instances (i.e. with exceptionally long runtimes or low quality schedules) as well as their influence to the accumulated metrics. Note that outliers have to be expected for every exact modulo scheduling approach due to the NP-hard nature of the underlying problem. However, if their number is small, this does not automatically impair the scheduler's practicability. Additionally, a benefit of the ILP method is that a quality measure (i.e. the branch-and-bound method's "gap" value [19]) is available for any feasible solution, which allows the practitioner to decide to try another scheduler (exact or heuristic) for the same problem.

We introduce the notion of the *best-known* interval  $\lambda^{\bullet}$  and tuple of interval and schedule length  $S^{\bullet}$  to indicate the best solution found across all experiments conducted in this work, i.e. including all approaches and configurations, as a reference point for loops for which the optimal solution is not known.

The **Moovac**-based schedulers outperform the other approaches in this experiment: The overall fastest configuration is Mv-I- $U_{Im}$ -1min with an accumulated runtime of 15.7 minutes for all 188 test instances. At first glance, Mv-S- $U_{Im}$ , which finds the highest-quality solutions overall, seemingly

has a significant advantage concerning the result quality over Mv-I- $U_{Im}$ , but note that the sum of II differences (column " $\lambda^{\circ} - \lambda^{\bullet} : \Sigma$ ") is concentrated on only three loops (column " $\lambda^{\circ} - \lambda^{\bullet} : \#$ "). For all other loops, Mv-I- $U_{Im}$  delivers results on par with its single-II variant (columns under "# loops with...").

Mv-S- $U_{Im}$  and Mv-I- $U_{Im}$  achieve the best trade-off between scheduler runtime and result quality already with the 1 minute time limit. The runtimes increase with the time budget, but the result quality is only improved marginally. In the 60 minute configurations, the solution space covered by the Moovac-based schedulers for some instances becomes too large to fit in the 16 GiB allocated to the experiments (column "OOM").

Mv-S clearly benefits, in terms of runtime and quality, from the presence of an upper bound for the schedule length, and from the tighter estimate that  $U_{Im}$  gives compared to  $U_{Eb}$ . This effect is even more pronounced for Mv-I, because the value of the bound directly affects the complexity of the Moovac-I-ILP.

Being an exact scheduler as well, **Eichenberger's** formulation achieves optimal results for almost as many loops as the Moovac-based approaches. However, especially with the smallest time budget, it is unable to find modulo schedules for several large instances (column "no *S*"), and runs out of memory for one loop. Given more time, the solution quality improves, but unfortunately four more loops cannot be scheduled within the 16 GiB memory limit. The accumulated runtimes and solution quality is mostly unaffected by the choice of schedule length bound.

As expected, the **ModuloSDC** algorithm is quite fast, but lacks the capability to prove optimality of its solutions, and misses the the best-known IIs by at least 77, spread over 20 instances. Starting with the 5 minute configuration, imposing an upper bound on the schedule length effectively serves as a third measure (besides time limit and backtracking budget) to give up on fruitless scheduling attempts, and limits the overall runtime to roughly 90 minutes. The quality metrics remain unchanged over the four different time limits. Curiously, without a bound, one more loop can be scheduled, which alone contributes an II difference of 77 to  $MSDC-U_{Eb}$  and  $-U_{Im}$ . The length of the schedule found by  $MSDC-\infty$  for this particular instance does not come near the values of the bounds, though.

The maximum ILP **construction time** per scheduling attempt is below 10 seconds for the exact approaches, and turned out to be negligible compared to the runtime of the solver's branch-and-bound algorithm. This excludes loops for which a scheduling attempt ran out of memory, because we did not record the precise point in time when the memory limit was hit. We also did not record a separate construction time for *MSDC*, as the linear program is constantly changed over the course of the algorithm.

The **performance profile** [10] in Figure 8 shows a different view on the dataset for the 5 minute experiment, as it relates the runtimes of different configurations for each individual loop: for every configuration  $\alpha$  and loop x, a ratio  $\rho_{\alpha,x}$  of the runtime of  $\alpha$  on x divided by the fastest runtime for x is computed. In the plot, we count the number of loops that have  $\rho_{\alpha,x} \leq \Theta$  for a given configuration  $\alpha$ , i.e. the number of loops whose runtime with  $\alpha$  is at most  $\Theta$  times worse than the best-known runtime for x. The performance profile can be interpreted as the probability<sup>12</sup> that a configuration is able to modulo schedule a loop within a certain performance envelope. The higher the curve the better. For example, the data point marked with dark blue square on the  $MSDC-U_{Eb}$  plot signifies that the scheduler runtime for 92 loops (=  $\frac{92}{188} \approx 49\%$ ) was at most twice as long as the fastest runtime across all configurations in the performance profile. The intersection of the plots with the

<sup>&</sup>lt;sup>12</sup>By dividing the loop counts by the total number of loops.



Fig. 8. Performance profile of the scheduling times (5 minute time limit), showing the number of loops for which a combination of an approach and a bound resulted in a scheduling time that is at most  $\Theta$  times slower than the fastest scheduling time for each individual loop. The table shows the values for the special case  $\Theta = 1$ , i.e. the number of loops for which a configuration defined the fastest scheduler runtime.

" $\Theta = 1$ "-line shows the number of loops for which a configuration set the fastest runtime. This data is repeated in the table below the performance profile.

We observe that  $M\nu$ -S- $U_{Im}$  is the fastest configuration to schedule 149 of the 188 loops ( $\approx$  79%), followed by  $M\nu$ -I- $U_{Im}$  with 18 fastest runtimes. In general, the plots for the Moovac-based configurations (excluding  $M\nu$ -S- $U_{Eb}$ ) and MSDC rise quickly, meaning that they are close to the fastest runtime for the majority of instances. The slope of the plots for EB indicates that for roughly 75 % of the loops, these configurations require at least 5x longer than the respective fastest configuration.

The performance profile also hints at a key **difference between** Mv-S and Mv-I. The single-II variant is often faster for an individual loop due to the simpler ILP formulation and the location of the first feasible interval in the II search space (cf. Fig. 7). However, the practical advantage of Mv-I is that its runtime is capped at  $2 \cdot \tau$  by design, which is reflected in the much faster accumulated runtimes shown in Table 5. As discussed above, the quality metrics in Table 6 show that this novel way of approaching the modulo scheduling problem comes with almost no loss in solution quality, with Mv-I- $U_{Im}$  missing the best-known interval  $\lambda^{\bullet}$  for at most two additional instances compared to Mv-S- $U_{Im}$ .

## 5.6 FPGA implementation

We used Nymble [18] to generate accelerator modules that are pipelined according to the schedules computed in the experiments with the  $U_{Im}$ -bound and 5 minute time limit. The non-loop parts of the applications, as well as loops for which a particular scheduler could not compute a feasible

Approa	ch / Bound	Loops classified by scheduler runtime (188 loops)										
		< 1	.0 s	10	-100 s	100-1k s		1k-10k s		$\geq 10k s$		all
		#Σ	[min]	# Σ	E [min]	# Σ	E [min]	#	Σ [min]	#	Σ [min]	$\Sigma$ [min]
	Time limit: 1 minute											
Mv-S	$\infty$	168	0.7	8	5.9	12	92.0	-	-	-	-	98.5
Mv-S	$U_{Eh}$	173	0.5	5	2.7	10	82.9	-	-	-	-	86.1
Mv-S	$U_{Im}^{Lv}$	174	0.4	5	2.8	9	71.3	-	-	-	-	74.5
Mv-I	$U_{Eb}$	155	0.7	32	26.1	1	2.0	-	-	-	-	28.7
Mv-I	$U_{Im}$	171	0.5	15	11.2	2	4.0	-	-	-	-	15.7
EB	$\infty$	165	1.2	10	6.1	8	60.0	5	112.6	-	-	179.8
EB	$U_{Eb}$	167	1.5	7	3.0	9	55.8	5	112.7	-	-	173.0
EB	$U_{Im}$	167	1.3	9	5.5	7	52.1	5	112.7	-	-	171.7
MSDC	~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	172	0.7	7	4.6	8	46.8	1	17.1	-	-	69.2
MSDC	$U_{Eb}$	172	1.0	8	5.5	7	44.1	1	16.8	-	-	67.5
MSDC	$U_{Im}$	172	1.0	8	5.4	7	44.4	1	16.9	-	-	67.8
					Time lin	nit: 5	minutes					
Mv-S	$\infty$	168	0.6	3	0.9	8	55.2	9	378.3	-	-	435.0
Mv-S	$U_{Eb}$	173	0.5	3	0.7	4	32.5	8	339.5	-	-	373.1
Mv-S	$U_{Im}$	174	0.4	3	0.8	3	25.1	8	292.8	-	-	319.1
Mv-I	$U_{Eb}$	155	0.7	11	4.2	22	117.1	-	-	-	-	122.0
Mv-I	$U_{Im}$	172	0.7	5	1.8	11	70.3	-	-	-	-	72.7
EB	$\infty$	165	1.2	10	6.4	5	39.8	8	520.1	-	-	567.6
ED ED	$U_{Eb}$	167	1.5	8	4.4	5	45.0	8	538.0	-	-	589.4
ED MSDC	$U_{Im}$	10/	1.5	9	5.0	4	24.8 22.9	2	04.0	-	-	595.2 194.1
MSDC	ш	172	0.7		4.0	6	20.5	2	94.9 40.0	-	-	124.1 86.1
MSDC	$U_{Eb}$	172	1.1	8	5.6	6	30.9	2	49.0	-	_	85.5
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		4.60	0.7				minules		050.4		005.0	
Mv-S	$\infty$	168	0.7	3	1.0	5	62.4	11	952.1	1	225.0	1241.1
Mv-S	$U_{Eb}$	173	0.5	3	0.7	3	37.5	8	797.1	1	225.0	1060.8
MV-S	$U_{Im}$	1/4	0.4	10	0.8	2	30.0	8	655.0	1	225.0	911.2
May I	$U_{Eb}$	171	0.5	12	4.5	20	120.2	2	49.0	-	-	344.4 212.0
FR	$\mathcal{O}_{Im}$	165	0.5	10	6.8	3	38.7	6	388.4	-	1058 5	1/03 5
EB	$U_{n}$	167	1.2	9	6.0	1	15.7	7	436.6	4	1058.5	1524.1
EB	U <sub>E</sub> b U <sub>I</sub>	167	1.3	9	5.0	2	22.8	6	407.5	4	1074.4	1511.2
MSDC	$\infty$	172	0.7	7	4.8	6	24.1	3	180.7	_		210.3
MSDC	$U_{Fh}$	170	0.8	10	5.9	6	31.3	2	54.1	-	-	92.1
MSDC	$U_{Im}^{Lv}$	170	0.7	10	5.8	6	31.2	2	52.8	-	-	90.6
Time limit: 60 minutes												
Mv-S	∞	168	0.7	3	0.9	1	2.4	6	422.8	10	5614.8	6041.7
Mv-S	$U_{Fh}$	173	0.5	3	0.7	1	7.3	2	120.0	9	4996.2	5124.7
Mv-S	$U_{Im}$	174	0.4	3	0.8	-	-	4	250.0	7	4380.1	4631.3
Mv-I	$U_{Eb}$	155	0.7	11	4.3	4	27.2	18	1140.2	-	-	1172.4
Mv-I	$U_{Im}$	172	0.7	5	1.8	-	-	11	720.9	-	-	723.4
EB	$\infty$	165	1.2	10	6.7	1	8.1	6	407.1	6	4877.1	5300.2
EB	$U_{Eb}$	167	1.5	9	5.7	-	-	6	508.8	6	4886.9	5403.0
EB	$U_{Im}$	167	1.4	9	5.3	1	7.4	5	373.4	6	4753.0	5140.4
MSDC	$\infty$	172	0.8	7	4.9	6	26.1	2	52.8	1	420.4	505.0
MSDC	$U_{Eb}$	171	0.9	9	5.8	6	32.7	2	49.6	-	-	89.0
MSDC	$U_{Im}$	172	1.1	8	5.8	6	32.3	2	49.4	-	-	88.6

Table 5. Scheduling times for combinations of approaches, time limits and bounds

The loops are classified into time brackets according to the scheduler runtime with a given approach. Columns "#" show the number of loops that fell into a particular time bracket. Columns " $\Sigma$ " show the accumulated scheduling time (in **minutes**) for the loops within a time bracket. Loops that caused the scheduler to run out of memory (cf. Table 6) are accounted for with the maximum available time to them, i.e. (min( $\lambda^{\top}, \lambda^{\perp} + 22$ ) –  $\lambda^{\perp} + 1$ ) ·  $\tau$  for the single-II approaches, and  $\tau$  for  $M\nu$ -I.

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Time limit: 60 minutes $Mv-S \propto$ 174       182       182       182       182       2       3       30       4
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Table 6. Schedule quality for combinations of approaches, time limits and bounds

The 188 loops are counted according to properties of the schedules (see Table 7) computed by a given approach.

Column	Description
$\lambda^* \square$ $\lambda^\circ = \lambda^*$ $\lambda^\circ = \lambda^\bullet$	counts loops where the II is <i>proven</i> to be optimal by the approach, as discussed in Section 4.1 counts loops where the II is equal to the <i>optimal</i> II counts loops where the II is equal to the <i>best-known</i> II across all experiments, including loops where $\lambda^{\bullet} = \lambda^{\star}$
$S^* \square$ $S = S^*$ $S = S^{\bullet}$	counts loops where the tuple $S = (\lambda^{\circ}, T_{\lambda^{\circ}})$ is <i>proven</i> to be optimal by the approach, as discussed in Section 4.1 counts loops where the tuple $S = (\lambda^{\circ}, T_{\lambda^{\circ}})$ is equal to the <i>optimal</i> solution counts loops where the tuple $S = (\lambda^{\circ}, T_{\lambda^{\circ}})$ is equal to the <i>best-known</i> solution across all experi- ments, including loops where $S^{\bullet} = S^{\star}$
no <i>S</i> OOM	counts loops for which no modulo schedule could be computed within the time and attempt limits counts loops for which no modulo schedule could be computed within the memory limit
$\lambda^{\circ} - \lambda^{\bullet}$ $\Sigma$ #	considers the differences between each loop's actual and best-known II. We set $\lambda^{\circ} = \lambda^{\top}$ in case no modulo schedule could be computed, and $\lambda^{\bullet} = \lambda^{\top}$ in case no modulo schedule is known (2 loops) accumulates the differences shows the number of loops that have $\lambda^{\circ} > \lambda^{\bullet}$





Application

Fig. 9. Maximum clock frequencies on Virtex-7 after HLS and place & route for the different schedulers  $(U_{Im}$ -5min configurations). Note that the Y-axis starts at 100 MHz.

modulo schedule, are not pipelined and rely on the non-modulo schedules computed by the heuristic fallback scheduler.

TaPaSCo [20] was employed to perform an out-of-context evaluation of these accelerators using Vivado 2018.2 targeting a Virtex-7 xc7vx690tffg1761-2 device. The target frequency was set to 200 MHz for the CHStone applications, MachSuite::aes and MachSuite::md\_grid, and to 320 MHz for the remaining MachSuite applications.

Figure 9 shows the maximum clock frequencies when using the modulo schedulers, in comparison to a entirely non-pipelined accelerator using only the fallback non-modulo schedule for all loops. Unfortunately, chstone::aes and chstone::jpeg could not be implemented on the target device due to routing congestion in the Nymble-generated microarchitecture for any scheduler configuration, and are therefore excluded in the plot.

Overall, the frequency variations when using the different schedulers are moderate, and none of the approaches (especially not the non-pipelined one) dominates this experiment. However, note that the scheduling step is so early in the HLS flow that it cannot directly influence the later decisions made by the logic synthesis tool that ultimately determine the design's cycle time, so this result is not unexpected.

# 6 CONCLUSION AND OUTLOOK

In this paper, we proposed Moovac, an exact bi-criteria formulation of the modulo scheduling problem (MSP) that integrates the actual II minimisation and can be solved optimally by a standard ILP solver.

An extensive experimental study in the context of a high-level synthesis compiler showed that combining the Moovac formulation with a short time limit of 1 or 5 minutes per candidate II, and an improved upper bound for the schedule length, results in a practically usable modulo scheduling approach that finds optimal IIs in over 95 % of the test instances, and near-optimal IIs otherwise.

Exact modulo scheduling is justified by the clearly higher quality of the solutions compared to a state-of-the-art heuristic approach. In the Moovac formulation, modelling the central moduloresource-constraints with overlap variables leads to overall shorter solver runtimes compared to a prior, well-tuned ILP formulation. Often, our approach is even faster than the heuristic SDC-based scheduler.

The focus of this work was to empirically study the practicability of a novel and exact formulation of the MSP. In the future, we plan to investigate how the model can be improved based on research into the more general class of cyclic scheduling problems in operations research literature.

MSP instances in the HLS context appear to comprise larger and denser dependence graphs than those that occur in VLIW compilers. This could by caused, for example, by the many resourceunconstrained operations, and the additional edges needed to model operator chaining. We plan to devise a formulation-independent problem reduction algorithm that abstracts non-critical subgraphs away in order to lower some of the HLS-specific complexity before passing the instance to the actual modulo scheduler.

Additionally, we would like to devise a generator for MSP instances in order to determine which problem sizes are still solvable in reasonable amounts of time, as well as to investigate which particular problem structures cause long runtimes for the various schedulers.

Lastly, the overlap variable-based combined modulo resource constraints in the Moovac formulation lend themselves to be selectively disabled. This can be exploited to perform exact *predicateaware* modulo scheduling, i.e., oversubscribing resource units with operations that are mutuallyexclusive at runtime.

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## ACKNOWLEDGMENTS

The experiments for this research were conducted on the Lichtenberg high performance computing cluster of TU Darmstadt. The authors would like to thank Xilinx, Inc. for supporting their work by hardware and software donations.

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Received February 2018; revised November 2018; accepted February 2019